

Case No. 62602

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NAME: JUSTIN GOREE

SIGNATURE: Justin Goree

22141 U.S. PTO

10/647069

08/22/03

Transmitted herewith for filing is the patent
application of:

Inventor: **Robert J. MEARS**

For: **SEMICONDUCTOR DEVICE INCLUDING MOSFET HAVING
BAND-ENGINEERED SUPERLATTICE**

[X] Patent Application: 36 pages, 71 claims.
[X] 9 sheets of drawings.
[X] The suggested drawing figure to be published is FIG. 1.
[X] Applicant qualifies as a small entity under 37 CFR § 1.27.
[X] Assignee info:

Name: **MEARS CORPORATION**
Address: **189 Wells Avenue, Suite 100, Newton, MA 02459**
State of Incorporation: **Massachusetts**

[X] Applicant claims priority benefit to the following U.S. applications:

Application No.: *****
Filing Date: **June 26, 2003**

Application No.: *****
Filing Date: **June 26, 2003**

[X] The Declaration and Filing Fee are NOT ENCLOSED.

[X] Name, Address and Citizenship of Inventors are as follows:

Robert J. MEARS
14 Summit Road
Wellesley, MA 02482
Citizen of United Kingdom

[X] **PLEASE ADDRESS ALL CORRESPONDENCE TO ATTORNEY OF RECORD: CHRISTOPHER F. REGAN**

[X] Associate this file with Customer No. 27975.



27975

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Date: August 22, 2003

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